

**IN THE CLAIMS**

1. (Currently Amended) A method for configuring and performing processing in a digital oscilloscope, comprising the steps of:

receiving one or more input parameters;

defining a plurality of processing elements based upon said ~~received~~ one or more input parameters;

assigning a graphical representative for each of the plurality of defined processing elements; and

connecting a plurality of said plurality graphical representatives corresponding to a plurality of the defined processing elements to define and graphically depict a processing web.

2. (Original) The method of claim 1, wherein at least two of said plurality of processing elements are updated at different speeds.

3. (Currently Amended) The method of claim 2, wherein a processing object controls the update of said at least two of said plurality of processing elements.

4. (Currently Amended) The method of claim 2, wherein one of said at least two of said plurality of processing elements operates at a ~~higher~~ an acquisition speed and another of said at least two of said plurality of processing elements operates at a ~~lower~~, display speed, and wherein the acquisition speed is higher than the display speed.

5. (Original) The method of claim 2, wherein said at least two of said plurality of processing elements are idle when not updated.

6. (Currently Amended) The method of claim 2, wherein one of said at least two of said plurality of processing elements is of a cumulative type running at a ~~higher~~ first speed, and

another of said at least two of said plurality of processing elements is of a non-cumulative type running at a ~~lower~~ second speed, and wherein the first speed is higher than the second speed.

7. (Currently Amended) A method of configuring and performing processing in a digital oscilloscope, comprising:

receiving one or more instructions;

defining a plurality of processing elements based upon said ~~received~~ one or more instructions;

assigning a graphical representative for each of the plurality of defined processing elements;

connecting a plurality of said ~~plurality of~~ graphical representatives of the defined processing elements to define and graphically depict a processing web for performing a desired processing; and

synchronizing said plurality of processing elements to generate a synchronized result.

8. (Original) The method of claim 7, wherein at least two of said plurality of processing elements are updated at different speeds.

9. (Original) The method of claim 8, wherein one of said plurality of processing elements requests required data from an upstream source.

10. (Original) The method of claim 9, wherein said request is made upon activation of an update pin of said one of said plurality of processing elements.

11. (Original) The method of claim 8, wherein one of said plurality of processing elements requests data from an upstream source when data is requested from it by a downstream processing element.

12. (Original) The method of claim 11, wherein no buffers are present between said plurality of processing elements.

13. (Original) The method of claim 7, wherein at least one of said plurality of processing elements receives M inputs on an input pin and produces N output results on an output pin, where M is an integer equal to or greater than 1 and where N is an integer equal to or greater than 0.

14. (Currently Amended) A method for configuring and performing processing in a digital oscilloscope, comprising the steps of:

defining a plurality of processing elements;

assigning a graphical representative for each of the plurality of defined processing elements;

connecting a plurality of said graphical representatives of the plurality of defined processing elements in a predetermined relationship to depict and allow for a desired processing; and

controlling said plurality of processing elements to manage the proper flow of data through the plurality of processing elements.

15. (Currently Amended) The method of claim 14, wherein said controlling of said plurality of processing elements is performed by updating one or more of said plurality of processing elements.

16. (Currently Amended) The method of claim 15, wherein a first of said plurality of processing elements is updated at a ~~higher~~ first speed, and a second of said plurality of processing elements is updated at a ~~lower~~ second speed, and wherein the first speed is higher than the second speed.

17. (Original) The method of claim 16, wherein the activation of said first and second processing elements are synchronized.

18. (Original) The method of claim 15, wherein said updating one or more of said plurality of processing elements is performed in response to ~~modification~~ notification that new acquisition data is available.

19. (Original) The method of claim 15, wherein said updating one or more of said plurality of processing elements is performed in response to a downstream request for data.

20. (Original) The method of claim 19, wherein said downstream request for data is made by a rendering processing object.

21. (Original) The method of claim 15, wherein said updating one or more of said plurality of processing elements is performed in response to a modification of the definition of any of said plurality of processing elements.

22. (Currently Amended) A processing web defining processing in a digital oscilloscope, comprising:

a plurality of processing elements that are defined based upon one or more received input parameters, each of said plurality of processing elements performing a discrete processing function, each of said plurality of defined processing elements having a graphical representative assigned thereto; and

a plurality of connections between said plurality of processing elements being generated in accordance with the manipulation of the graphical representatives corresponding to said plurality of processing elements and an indication of connections therebetween to define a flow of information therebetween

23. (Original) The processing web of claim 22, wherein at least two of said plurality of processing elements are updated at different speeds.

24. (Original) The processing web of claim 23, wherein a processing object controls the update of said at least two of said processing elements.

25. (Currently Amended) The processing web of claim 23, wherein one of said at least two of said plurality of processing elements operates at a ~~higher~~ an acquisition speed and another of said at least two of said plurality of processing elements operates at a ~~lower~~, display speed, and wherein the acquisition speed is higher than the display speed.

26. (Original) The processing web of claim 23, wherein said at least two of said plurality of processing elements are idle when not updated.

27. (Currently Amended) The processing web of claim 23, wherein one of said at least two of said plurality of processing elements is of a cumulative type running at a ~~higher~~ first speed, and another of said at least two of said plurality of processing elements is of a non-cumulative type running at a ~~lower~~ second speed, and wherein the first speed is higher than the second speed.

28. (Currently Amended) A processing web defining processing in a digital oscilloscope, comprising:

a plurality of processing elements defined in accordance with one or more received instructions, each of said plurality of defined processing elements having a graphical representative assigned thereto; and

a plurality of defined data connections connecting said plurality of processing elements being generated in accordance with the manipulation of the graphical representatives

corresponding to said plurality of processing elements and an indication of connections  
therebetween to define a processing web for performing a desired processing;

wherein said plurality of processing elements are synchronized to generate a  
synchronized result.

29. (Original) The processing web of claim 28, wherein at least two of said plurality  
of processing elements are updated at different speeds.

30. (Original) The processing web of claim 28, wherein one of said plurality of  
processing elements requests required data from an upstream source.

31. (Original) The processing web of claim 30, wherein said request is made upon  
activation of an update pin of said one of said plurality of processing elements.

32. (Original) The processing web of claim 28, wherein one of said plurality of  
processing elements requests data from an upstream source when data is requested from it by a  
downstream processing element.

33. (Original) The processing web of claim 32, wherein no buffers are present  
between said plurality of processing elements.

34. (Original) The processing web of claim 28, wherein at least one of said plurality  
of processing elements receives M inputs on an input pin and produces N output results on an  
output pin, where M is an integer equal to or greater than 1 and where N is an integer equal to or  
greater than 0.

35. (Currently Amended) A processing web defining processing in a digital  
oscilloscope, comprising:

a plurality of processing elements, each of said plurality of defined processing elements  
having a graphical representative assigned thereto; and

a plurality of defined connections for connecting a plurality of said plurality of processing elements in a predetermined relationship being generated in accordance with the manipulation of a plurality of the graphical representatives corresponding to said plurality of processing elements and an indication of connections therebetween to allow for a desired processing;

wherein said plurality of processing elements are controlled to manage the proper flow of data through the plurality of processing elements.

36. (Original) The processing web of claim 35, wherein said controlling of said plurality of processing elements is performed by updating one or more of said plurality of processing elements.

37. (Currently Amended) The processing web of claim 36, wherein a first of said plurality of processing elements that is to be updated at a higher first speed, and a second of said plurality of processing elements that is to be updated at a lower second speed, and wherein the first speed is higher than the second speed.

38. (Original) The processing web of claim 37, wherein the updating of said first and second processing elements are synchronized.

39. (Original) The processing web of claim 36, wherein said updating one or more of said plurality of processing elements is performed in response to notification that new acquisition data is available.

40. (Original) The processing web of claim 36, wherein said updating one or more of said plurality of processing elements is performed in response to a downstream request for data.

41. (Original) The processing web of claim 40, wherein said downstream request for data is made by a rendering processing object.

42. (Original) The processing web of claim 36, wherein said updating one or more of said plurality of processing elements is performed in response to a modification of the definition of any of said plurality of processing elements.